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**** CONTINUING DATA *******
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**** FOREIGN APPLICATIONS *******
 JAPAN 2003-435269 12/26/2003
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35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No		JAPAN	30	39	7
Verified and /NAM THANH NGUYEN/ Acknowledged Examiner's Signature	Initials				

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TITLE
 Memory device, memory circuit and semiconductor integrated circuit having variable resistance

FILING FEE RECEIVED 2650	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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